



- PCB VCXO 25×15mm
 - DIP TYPE Design
 - Mode : Fundamental, Multi
 - HCMOS/ TTL OUTPUT
- Applications : Network equipment etc.

Standard Specification

Frequency Range	1.000MHZ to 160.000MHZ	
Supply voltage	3.3V	5.0V
Pin 1 Control voltage	1.65V±1.35V (1.65V)	2.5V±2.0V (2.5V)
Frequency Deviation	±50ppm min	±50ppm min
	±100ppm min	±100ppm min
	±150ppm min	±150ppm min
Current consumption	50mA max	65mA max
Linearity	10% max	
Slope	Positive	
Temperature Range	Operating	-40°C to +85°C, User spec
	Storage	-55°C to +125°C
Frequency Stability	±50ppm max, User spec	
Output load condition	CMOS 15pF/ 10TTL	
Symmetry (Duty)	40% to 60%(STD), 45% to 55% (option)	
Output rise and fall time (tr/tf)	7ns max (1.000MHZ ~ 30.000MHZ) 5ns max (30.001MHZ ~ 70.000MHZ) 4ns max (70.001MHZ ~ 160.000MHZ)	
High output voltage	90% Vdd min / 2.4Vdd min	
Low output voltage	10% Vdd max / 0.4Vdd max	
Oscillation start up time	5ms max	
Aging	± 3ppm max	
phase Jitter (12KHZ ~20MHZ)	100ps max	
Period Jitter (Pk-Pk)	25ps max	

Dimensions (mm)

